

Lecture 04.02 DC motor driving

There are two common methods of driving DC motors: (a) with a digital motor driver and (b) with an analog amplifier. Schematics of both are shown in Figure 04.2.

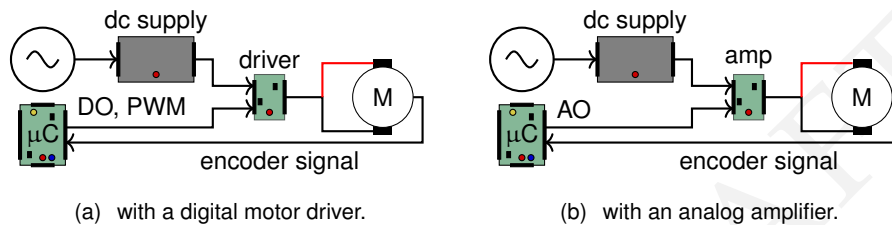


Figure 04.2: two common methods of driving motors.

04.02.1 Digital motor drivers

A microcontroller such as the myRIO or Arduino can easily produce a PWM signal, which, as we saw in Lecture 04.01, can be averaged by a system's dynamics such that varying the duty cycle varies the averaged signal. However, microcontrollers are *low-power* and cannot drive even small DC motors. Therefore it is common to include a special kind of integrated circuit (IC) that uses the microcontroller's low-power PWM signal to gate a high-power DC source signal for delivery to the motor. These are called *digital motor drivers*; a common system setup with a motor driver is shown in Figure 04.2(a). They deliver power from a high-power source in accordance with a PWM signal, and they often include many additional features such as

digital motor
drivers

1. compact forms;
2. forward- and reverse-driving (see Lecture 04.02.1.1)
3. protection against reverse voltage, overcurrent, and overheating; and
4. output pins that monitor delivered current and voltage.

These digital motor drivers are sometimes called *class-D* or *switching amplifiers*. Generally, they are inexpensive and are quite efficient (around 90 % in some cases), which, in addition to conserving power, adds the capacity of delivering high-power operation or requiring lower heat dissipation (or a "Goldilocks" mixture thereof).

class-D amps
switching amps

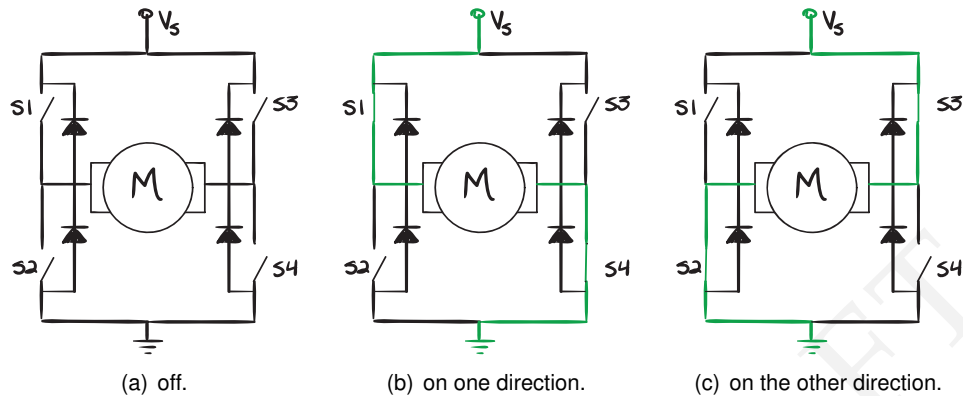


Figure 04.3: H-bridge operation.

04.02.1.1 H-bridge circuits

We want to drive DC motors at different effective voltages *and* different directions. An H-bridge circuit allows us to reverse the direction of the PWM signal delivered to the motor. Figure 04.3 is a diagram of the H-bridge circuit.

The switches S_1 - S_4 are typically instantiated with MOSFET transistors. As shown in the figure below, during the high duration of the PWM pulse, either S_1 and S_4 (Figure 04.3(b)) or S_2 and S_3 (Figure 04.3(c)) are closed and the others are open.

Recall that the electronics side of a DC motor can be modeled as a resistor and inductor in series with an electro-mechanical transformer. The inductance of the windings make it an “inductive” load, which presents the following challenge. We can’t rapidly change the current flow through an inductor without a huge spike in voltage, and the switches do just that, leading to switch damage. Therefore, during the low or “off” duration of the PWM signal, S_1 - S_4 cannot all be simply opened. There are actually a few options for switch positions that allow the current to continue to flow without inductive “kickback.”

What’s up with the diodes? Technically, they could be used to deal with the kickback. But since the diodes dissipate power, the proper switching is the primary kickback mitigation technique. However, the diodes ease the transition between switch flips, which are never quite simultaneous.

Table 04.1: comparison between digital motor drivers and linear analog amplifiers.

feature	digital motor driver	analog amplifier
cost	less expensive	more expensive
signal noise	noisy	minimal
audible noise	load	none
low-signal fidelity	poor	good
high-precision control	poor	good
efficiency	90 %	50 %
heat generation	low	high
high-powered	> 100 W	< 100 W
brushed/less dc	good	good
H-bridge	required	not required

04.02.2 Analog amplification

An alternative to digital motor drivers are analog amplifiers, which require a slightly different setup, shown in [Figure 04.2\(b\)](#). This setup requires an analog signal from the microcontroller, a digital device. Therefore, the microcontroller performs a process called *digital-to-analog conversion* (DAC), treated further in [Lecture 06.01](#) and [Resource 14](#). Many microcontrollers have this functionality and can produce analog signals over ranges such as ± 10 V, the range of the myRIO's CIO channel analog outputs.

DAC

An amplifier essentially “adds power” to the microcontroller analog output from an external power source. There are several varieties that can operate as voltage/current-controlled voltage/current sources within a range of operation. When that range is exceeded, operation typically becomes nonlinear and finally saturates (increased input does not increase output). Saturation is, of course, one of several considerations when designing with amplifiers.

A comparison between digital motor drivers and analog amplifiers is given in [Table 04.1](#). For more, see ([Collins, 2018](#)).

04.02.3 The ECL instantiation

The Embedded Computing Lab (ECL) has both digital motor drivers and linear analog amplifiers, both of which are commercially available.

04.02.3.1 Digital motor drivers

For a digital motor driver, we use a connectorized printed circuit board (PCB)—the Pololu motor driver carrier:

`pololu.com/product/1451`
`ricopic.one/resources/pololu_VNH5019.pdf`. (manual)

This includes an STMicroelectronics VNH5019 H-bridge motor driver integrated circuit:

`ricopic.one/resources/vnh5019.pdf`.

This type of motor driver is commonly found in small-motor applications such as those in an automobile used for adjusting seat, window, and mirror positions.

04.02.3.2 Analog amplifiers

The Copley Controls 412 voltage-controlled current source (or *transconductance*) amplifiers in the ECL are actually switched amplifiers, internally (so they're relatively efficient and capable of high-power), but function as analog amplifiers. This is a standard type of motor amplifier found in industrial settings. The device manual can be found here:

`ricopic.one/resources/Copley412.pdf`.

For the amplifier settings used in the ECL, see [Resource 10](#).