

## HC12 ADDRESSING MODES

ADDRESS MODE	FORM	EXAMPLE
EXTENDED	16-Bit Address is part of the instruction	LDAA \$122A STAA \$8A5 ADDA \$652
DIRECT	MS Byte of Address is 00	LDAA \$12 ADDA \$52
INHERENT	Instructions requiring no Address	ABA CLRA INX
IMMEDIATE	Data is in the Instruction	LDAA #\$122 ADDA #7 ORAA #11010101B
RELATIVE	Address is sum of current PC and an Offset in the instruction (Used for Branch Instructions)	BCC LABEL BRA HERE
INDEXED	Address is the sum of the Offset byte in the instruction and the value in the Index Register	LDAA \$52, X
INDEXED-Indirect	Address is in memory at the address indicated by the sum of the Offset byte in the instruction and the value in the Index Register	LDS [D, X]